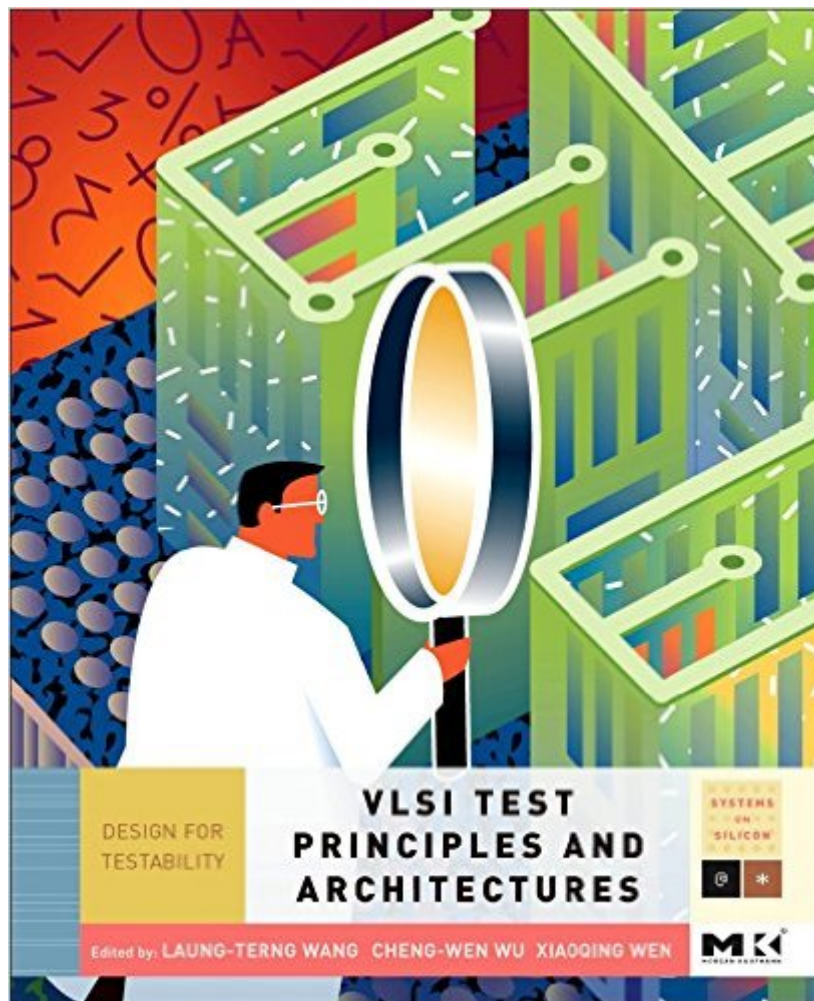


The book was found

VLSI Test Principles And Architectures: Design For Testability (The Morgan Kaufmann Series In Systems On Silicon)



Synopsis

This book is a comprehensive guide to new DFT methods that will show the readers how to design a testable and quality product, drive down test cost, improve product quality and yield, and speed up time-to-market and time-to-volume. Â Most up-to-date coverage of design for testability. Â Coverage of industry practices commonly found in commercial DFT tools but not discussed in other books. Â Numerous, practical examples in each chapter illustrating basic VLSI test principles and DFT architectures.Â Lecture slides and exercise solutions for all chapters are now available.Â Instructors are also eligible for downloading PPT slide files and MSWORD solutions files from the manual website.

Book Information

Series: The Morgan Kaufmann Series in Systems on Silicon

Hardcover: 808 pages

Publisher: Morgan Kaufmann; 1 edition (July 21, 2006)

Language: English

ISBN-10: 0123705975

ISBN-13: 978-0123705976

Product Dimensions: 2 x 7.5 x 9.2 inches

Shipping Weight: 4 pounds (View shipping rates and policies)

Average Customer Review: 5.0 out of 5 starsÂ Â See all reviewsÂ (3 customer reviews)

Best Sellers Rank: #935,844 in Books (See Top 100 in Books) #44 inÂ Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI #126 inÂ Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Integrated #127 inÂ Books > Computers & Technology > Hardware & DIY > Microprocessors & System Design > Computer Design

Customer Reviews

I have been reading this book for my new role as a DFT Engineer. I must say the concept in this book are very practical. I have been using this book as a reference book along side the requirements of the chip and it has been an invaluable resource.I will definitely recommend this book to anyone trying to understand DFT principles or use it as s refernce as part of DFT engineer role.Cheers...

I co-authored a chapter, so I am biased. But I use this book in my graduate test class. It is an

excellent text for covering all of the fundamentals of integrated circuit testing - basic design-for-test, and algorithms for test generation and fault simulation.

This is a great book for Test/DFT engineers and EDA engineers developing test tool. It gives a thorough review of lot of concepts and techniques employed in practice which cannot be found if you look at a general testing book. This also makes it an excellent resource to prepare for interviews.

[Download to continue reading...](#)

VLSI Test Principles and Architectures: Design for Testability (The Morgan Kaufmann Series in Systems on Silicon) Computers as Components, Third Edition: Principles of Embedded Computing System Design (The Morgan Kaufmann Series in Computer Architecture and Design) Computers as Components: Principles of Embedded Computing System Design (The Morgan Kaufmann Series in Computer Architecture and Design) Transactional Information Systems: Theory, Algorithms, and the Practice of Concurrency Control and Recovery (The Morgan Kaufmann Series in Data Management Systems) Computer Organization and Design, Fourth Edition: The Hardware/Software Interface (The Morgan Kaufmann Series in Computer Architecture and Design) Computer Organization and Design, Third Edition: The Hardware/Software Interface, Third Edition (The Morgan Kaufmann Series in Computer Architecture and Design) Computer Organization and Design: The Hardware Software Interface: ARM Edition (The Morgan Kaufmann Series in Computer Architecture and Design) Skew-Tolerant Circuit Design (The Morgan Kaufmann Series in Computer Architecture and Design) Pervasive Games: Theory and Design (Morgan Kaufmann Game Design Books) Digital Watermarking (The Morgan Kaufmann Series in Multimedia Information and Systems) How to Build a Digital Library (Morgan Kaufmann Series in Multimedia Information and Systems (Paperback)) VLSI Fabrication Principles: Silicon and Gallium Arsenide, 2nd Edition Circuits, Interconnections, and Packaging for Vlsi (Addison-Wesley VLSI systems series) Distributed Algorithms (The Morgan Kaufmann Series in Data Management Systems) Spatial Databases: With Application to GIS (The Morgan Kaufmann Series in Data Management Systems) Routing, Flow, and Capacity Design in Communication and Computer Networks (The Morgan Kaufmann Series in Networking) ARM System Developer's Guide: Designing and Optimizing System Software (The Morgan Kaufmann Series in Computer Architecture and Design) Foundations of Analog and Digital Electronic Circuits (The Morgan Kaufmann Series in Computer Architecture and Design) Computer Architecture, Fifth Edition: A Quantitative Approach (The Morgan Kaufmann Series in Computer Architecture and Design) Computer Architecture: A Quantitative Approach (The Morgan Kaufmann Series in

